



JITENDRA KUMAR GHANSHYAM PANDIT

Lecturer

Electrical Engineering

Department,

Government Polytechnic, Valsad

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EDUCATIONAL QUALIFICATION

B.E. in Electrical Engineering

2009

Veer Narmad South Gujarat University, Surat

M.Tech in Power Electronics & Drives

2013

S.V. National Institute of Technology, Surat.

WORK EXPERIENCE

Government Polytechnic, Valsad

2016-Present

Lecturer

TECHNICAL SKILLS AND KNOWLEDGE

Computer Aided Design Packages : MATLAB, Psim

SUBJECT TAUGHT

Electronic Circuits and Components

Digital Electronics and Digital Instruments

DC Machines and single phase transformer

Transmission and Distribution of Electrical Power

Power Electronics

TRAINING AND WORKSHOP

- “Comprehensive Online Intellectual Property Rights” at online during 07/06/2020 to 14/09/2020 (2 Week).
- Accreditation and Outcome based Learning at NPTEL MOOC during 26/08/2019 to 17/11/2019 (2 Week).
- “Fundamental of Power Electronics” at NPTEL MOOC during 28/01/2019 to 19/04/2019 (2 Week)
- “Power System Analysis” at NPTEL MOOC during 30/07/2018 to 19/10/2018 (2 Week)
- “Induction Phase-I” at NITTTR Ahmedabad during 25/06/2018 to 06/07/2018 (2 Week)

PORTFOLIOS (Current Portfolios)

- Department Student Scholarship and its related work.
- NBA criteria 3 co-coordinator.
- Class coordinator of 3rd semester E3 division.

RESEARCH PROJECTS GUIDED

NIL

PUBLICATION

Journal Papers

NIL

Conferences Papers

NIL

ACADEMIC PROJECTS GUIDED

Control of home appliances with Bluetooth device
Smart street Lightning system
Solar powered auto irrigation system
Undervoltage and overvoltage Protection of household equipments
Waste management system

PATENT FILED

NIL

PROFESSIONAL INSTITUTION MEMBERSHIPS

- Life Time Member-Indian Society for Technical Education

EXPERT LECTURES

NIL

AWARDS

NIL